



Outstanding performance for mid-range mobile phones

Philips NEXPERIA cellular multimedia baseband PNX5230 for GSM/GPRS/EDGE handsets

This baseband, used in the successful NEXPERIA cellular system solution 5210, improves performance while lowering overall cost in mid-range GSM/GPRS/EDGE mobile phones. It combines very high integration with an exceptionally efficient architecture and eliminates the need for a separate coprocessor or audio chip.

Key features

- Architecture optimized for multimedia functionality
 - 130-MHz ARM946E-S system controller
 - 117-MHz 16-bit Philips Adelante™ DSP
 - Large on-chip instruction and data caches
 - Integrated peripheral connectivity and audio features
- Superior performance (as measured on the NEXPERIA cellular system solution 5210)
 - 95% CPU availability during GSM speech call
 - 85% CPU availability during data transfer EGRPS C8 MCS9 Data Link over EDGE
 - Data transfer downlink of up to 220 kbps (EDGE Class 10)
- Comprehensive connectivity interfaces
 - SPI, UART, I²C, USB, MMC/SD, SIM
 - Stereo AUX in/out audio with accessory detection
- Fully integrated 1.3-Mpixel camera interface with hardware JPEG encoder
- Dynamic downloading for new multimedia codecs
- Pre-integrated UMA codecs
- Built-in security functions (DRM1.0, customer OTP)
- Low power consumption
- Small, lead-free LFBGA280 package (11 x 11 mm)
- Easy upgrade from PCF5212/5213 basebands
- Flexible design tools for rapid design-in

The NEXPERIA cellular baseband PNX5230, a core component of the proven NEXPERIA cellular system solution 5210, offers superior performance while lowering overall cost in mid-range (2.75G) GSM/GPRS/EDGE mobile phones.

It integrates an ARM9 system controller, a 16-bit DSP, embedded memories, connectivity interfaces, a high-performance camera interface that supports fast preview, overlay, resizing, and rotation, plus a stereo AUX audio interface with accessory detection. As a result, it delivers superior performance without an external coprocessor or audio chip.

The PNX5230 uses the same basic architecture as the PCF5212 and PCF5213 basebands, so it offers an easy upgrade for existing phone designs.

Exceptionally efficient architecture

The PNX5230 is a full quad-band solution, supporting operation in 850, 900, 1800, and 1900 MHz bands. Manufactured in a 90-nm CMOS process, it combines an ARM946E-S system controller, running at up to 130 MHz, with a 16-bit Philips Adelante DSP RD16024 running at up to 117 MHz.

PHILIPS

For memory resources, the baseband has large instruction and data caches that deliver significant performance increases. It also has an enhanced modular DMA controller, and a DSP memory PROM of 136 kwords and SYROM of 126.5 kwords.

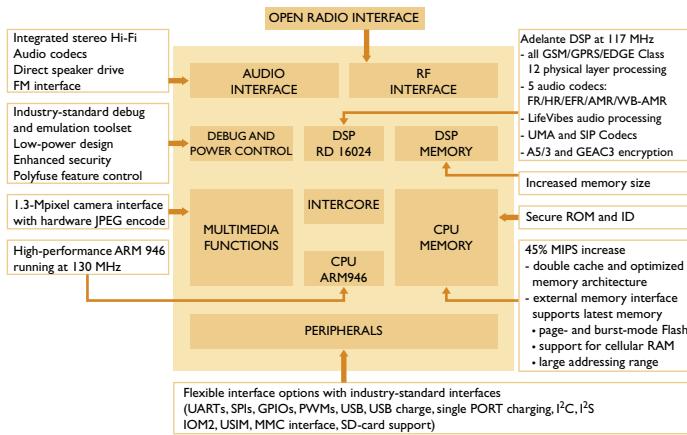
To significantly reduce power consumption, the core uses a reduced supply of 1.2 V. Using separate clocks for the DSP, the ARM9 controller, and the analog subsystems, reduces power consumption further.

Mono Interference Cancellation (MIC)

The PNX5230 is pre-loaded with MIC, the Philips implementation of Single Antenna Interference Cancellation (SAIC). MIC increases network capacity, improves dropped-call rates in situations with strong interference, and reduces re-transmission rates to deliver a higher effective data rate for downloads.

Extended multimedia features

On-chip audio features include a full stereo audio codec (Rx and Tx) with a sampling frequency up to 48 kHz plus functionality for audio mixing and equalizing. The baseband also includes built-in support for exceptional hands-free performance.



With the appropriate software, the PNX5230 can support video and audio streaming, playback of MP3 and AAC/AAC+ audio files, 64-channel stereo polyphonic sounds, and stereo audio (input and output) – all without an external coprocessor or audio chip.

Camera interface

The fully integrated camera interface supports 1.3-Mpixel camera sensors and complies with CCIR656. It has a built-in JPEG encoder and enables simultaneous audio/video capture. It supports up to 20 fps at QCIF+ resolution and performs real-time overlay processing. It offers multishot capabilities, a fast preview rate of 25 fps with direct stream from the sensor to the display, can flip and rotate pictures, and supports 4x digital zoom.

Connectivity features

The PNX5230 offers a comprehensive set of connectivity features, including SPI, a 1.8-Mbps UART, I²C, and a USB 2.0 full-speed (12 Mbps) device with a transceiver. The PNX5230 also offers stereo AUX audio with accessory detection, an MMC/SD flash card interface, and a SIM interface. UMA and SiP codecs are pre-integrated.

Built-in security features

To protect valuable content and ensure safe transactions, the PNX5230 offers a comprehensive set of built-in security features, including enhanced DRM 1.0, A5/3 and GEAC3 hardware accelerators, and customer-programmable OTP.

Telecom firmware

The PNX5230 is delivered with complete telecom firmware, so there's no need to modify the DSP subsystem. To simplify integration of the baseband into the handset stack, the core software includes a set of drivers/handlers as example code.

Functional blocks of the PNX5230

Philips Semiconductors

Philips Semiconductors is one of the world's top semiconductor suppliers, with 20 manufacturing and assembly sites and a sales organization that delivers in 60 countries.

For a complete up-to-date list of our sales offices please visit our website <http://www.semiconductors.philips.com/sales>

©2006 Koninklijke Philips Electronics N.V.

All rights reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.

date of release: January 2006

document order number: 9397 750 15434

Printed in the Netherlands

